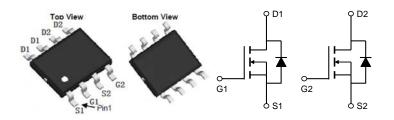


General Description

The KSP4882 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible onresistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

SOP-8 Pin Configuration



Product Summary

V _{DS} (V)	$R_{DS(on)}$ (m Ω)	I _D (A)
40	17 at VGS = 10 V	7
	22 at V _{GS} = 4.5 V	4.6

Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _G s	Gate-Source Voltage	±20	V
I n	Drain Current – Continuous (T _C =25°C)	7	А
lD	Drain Current – Continuous (Tc=100°C)	4.2	А
I _{DM}	Drain Current – Pulsed¹	37	А
P _D	Power Dissipation (Tc=25°C)	2	W
	Power Dissipation (Tc=100℃)	0.05	W/℃
T _{STG}	Storage Temperature Range	-55 to 150	℃
TJ	Operating Junction Temperature Range	-55 to 150	℃

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		40	°C/W
Rejc	Thermal Resistance Junction to Case		2	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted) Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V
I _{DSS}	Desir Course Lealers Course	V _{DS} =40V , V _{GS} =0V , T _J =25℃			1	uA
	Drain-Source Leakage Current	V _{DS} =40V , V _{GS} =0V , T _J =125℃			15	uA
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =6A		17	22	mΩ
		V _{GS} =4.5V , I _D =4A		22	28	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.5	2.4	V
gfs	Forward Transconductance	V _{DS} =5V , I _S =5A		20		S

Dynamic and switching Characteristics

Qg	Total Gate Charge		 26	
Q_{gs}	Gate-Source Charge	V_{DS} =20V , V_{GS} =10V , I_{D} =6A	 4.6	 nC
Q_{gd}	Gate-Drain Charge		 6.2	
$T_{d(on)}$	Turn-On Delay Time		 7	
Tr	Rise Time	V _D s=20V,R _L =2.5Ω	 15	 ns
$T_{d(off)}$	Turn-Off Delay Time	Vgs=10V,Rg=3Ω	 26	 115
T_f	Fall Time		 14	
Ciss	Input Capacitance		 980	
Coss	Output Capacitance	V_{DS} =20V , V_{GS} =0V , F =1MHz	 116	 pF
C _{rss}	Reverse Transfer Capacitance		 102	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			7	Α
lsм	Pulsed Source Current				30	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25℃			1.2	V

Note:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics (Curves)

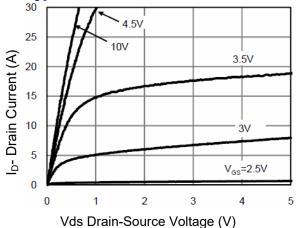


Fig.1 Output Characteristics

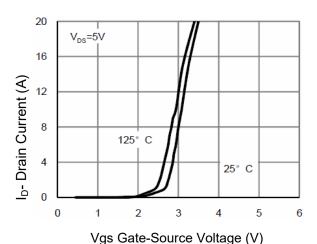


Fig. 2 Transfer Characteristics

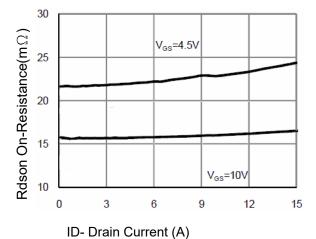


Fig. 3 Drain-Source On-Resistance

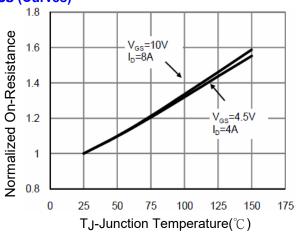


Fig. 4 Rdson-JunctionTemperature

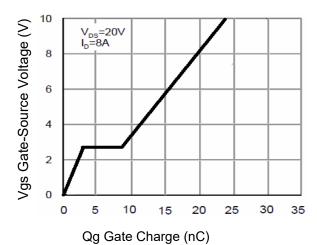
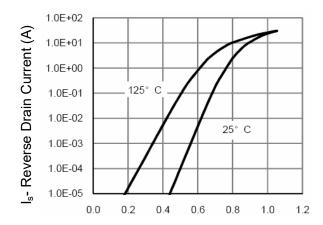


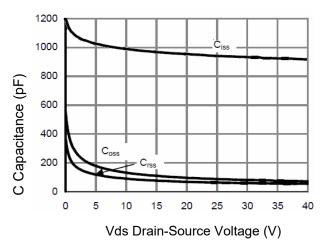
Fig. 5 Gate Charge



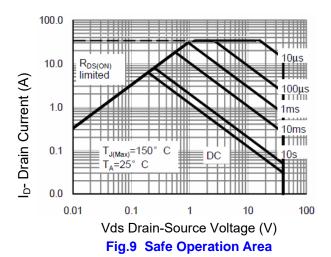
Vsd Source-Drain Voltage (V)

Fig. 6 Source- Drain Diode Forward





Figu.7 Capacitance vs Vds



1.5 (M) Le MO 0.5 1 T_A=25°C 0 20 40 60 80 100 120 140 TJ-Junction Temperature(°C)

Fig. 8 Power Dissipation

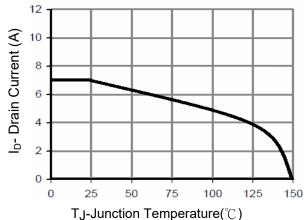


Fig. 10 V_{GS(th)} vs Junction Temperature

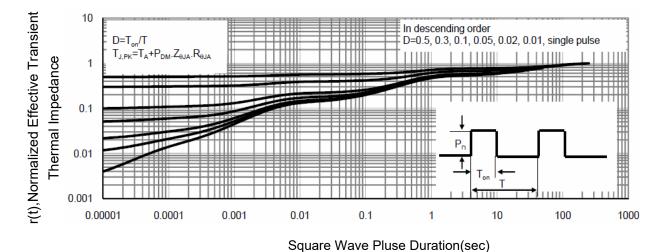


Fig.11 Normalized Maximum Transient Thermal Impedance



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