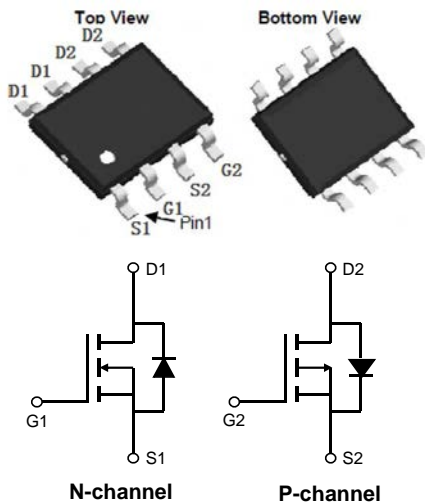


N&P-Channel complementary Power MOSFET

General Description

The KSP4616 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

SOP-8 Pin Configuration



Product Summary

	V_{DS} (V)	$R_{DS(on)}$ (m Ω)	I_D (A)
N-ch	30	12 at $V_{GS} = 10$ V	9
		19 at $V_{GS} = 4.5$ V	7.6
P-ch	-30	12 at $V_{GS} = 10$ V	-9
		18 at $V_{GS} = 4.5$ V	-7.7

Features

- High density cell design for ultra low R_{dson}
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Applications

- H-bridge
- Inverters

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	N-ch Rating	P-ch Rating	Units
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	9	-9	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	6.6	-6.8	A
I_{DM}	Drain Current – Pulsed ¹	48	-48	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	1.5		W
	Power Dissipation ($T_c=100^\circ\text{C}$)	0.05		W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	51	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	3.2	$^\circ\text{C}/\text{W}$

N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V, V _{GS} =0V, T _J =25°C	---	---	1	μA
		V _{DS} =30V, V _{GS} =0V, T _J =125°C	---	---	10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =8A	---	12	16	mΩ
		V _{GS} =4.5V, I _D =6A	---	19	25	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	1.0	1.5	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _S =6A	---	20	---	S

Dynamic and switching Characteristics

Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _D =8A	---	22	---	nC
Q _{gs}	Gate-Source Charge		---	4.5	---	
Q _{gd}	Gate-Drain Charge		---	4	---	
T _{d(on)}	Turn-On Delay Time	V _{DS} =15V, I _D =8A V _{GS} =10V, R _G =1Ω	---	8	---	ns
T _r	Rise Time		---	7	---	
T _{d(off)}	Turn-Off Delay Time		---	26	---	
T _f	Fall Time		---	9	---	
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, F=1MHz	---	1560	---	pF
C _{oss}	Output Capacitance		---	210	---	
C _{rss}	Reverse Transfer Capacitance		---	190	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	9	A
I _{SM}	Pulsed Source Current		---	---	18	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V

N-Channel Typical Electrical and Thermal Characteristics (Curves)

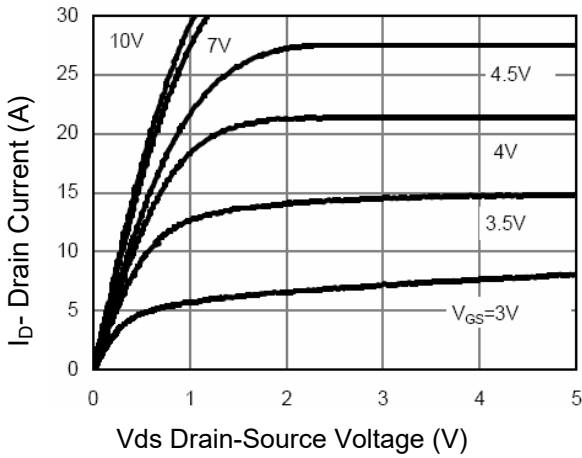


Fig. 1 Output Characteristics

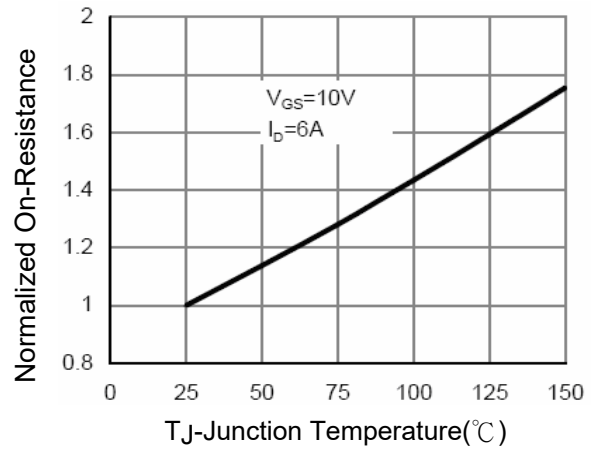


Fig. 4 Drain-Source On-Resistance

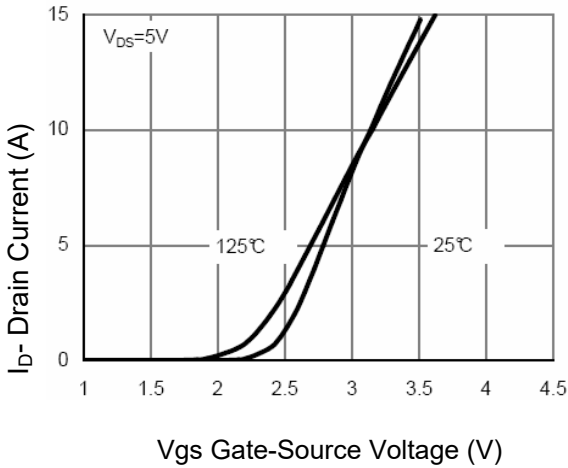


Fig. 2 Transfer Characteristics

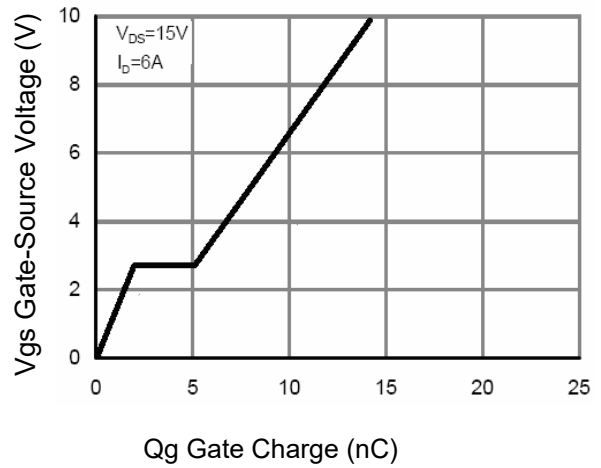


Fig. 5 Gate Charge

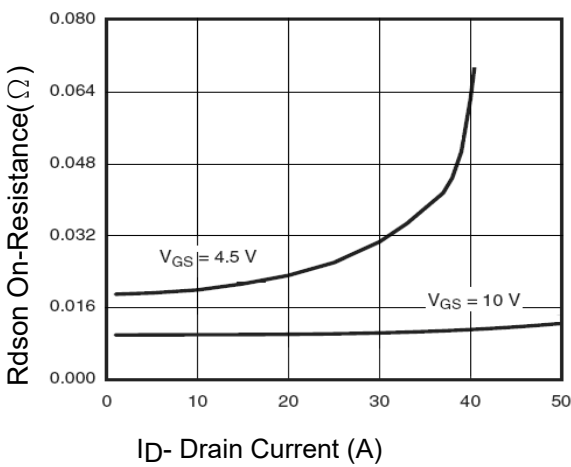


Fig. 3 Rdson- Drain Current

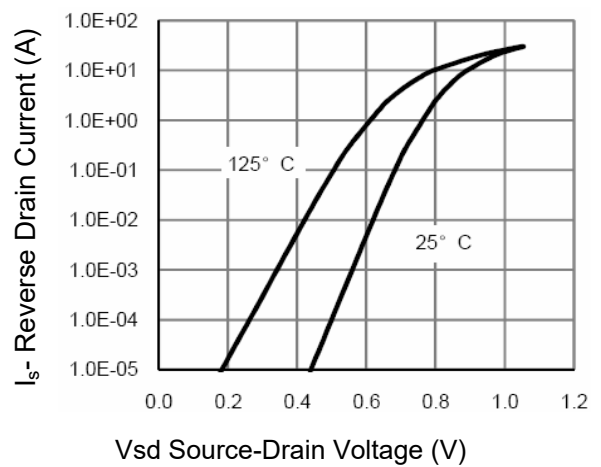


Fig. 6 Source- Drain Diode Forward

N&P-Channel complementary Power MOSFET

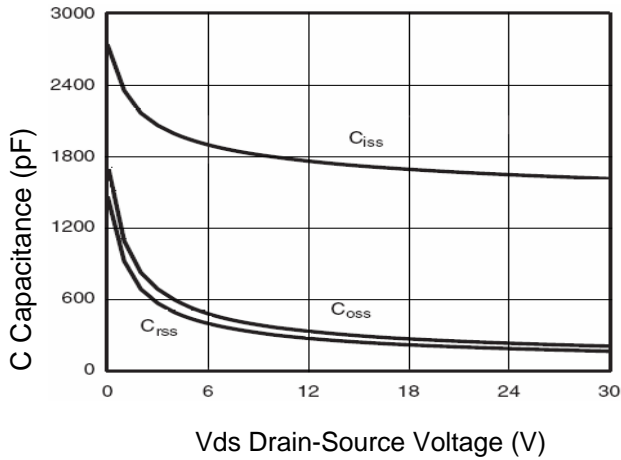


Fig.7 Capacitance vs Vds

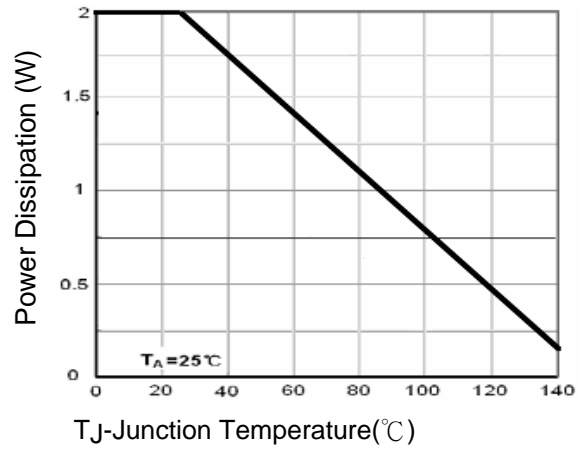


Fig. 8 Power De-rating

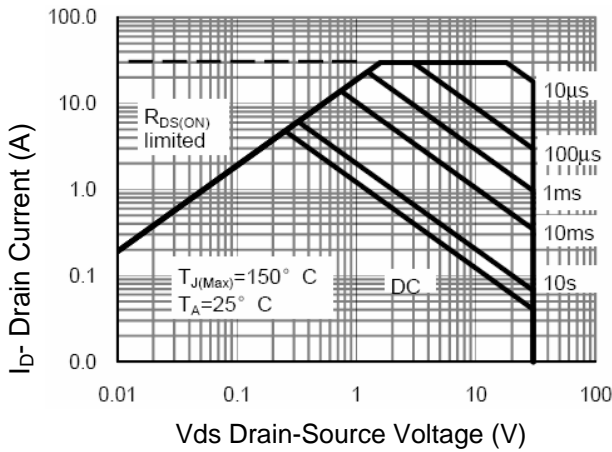


Fig.9 Safe Operation Area

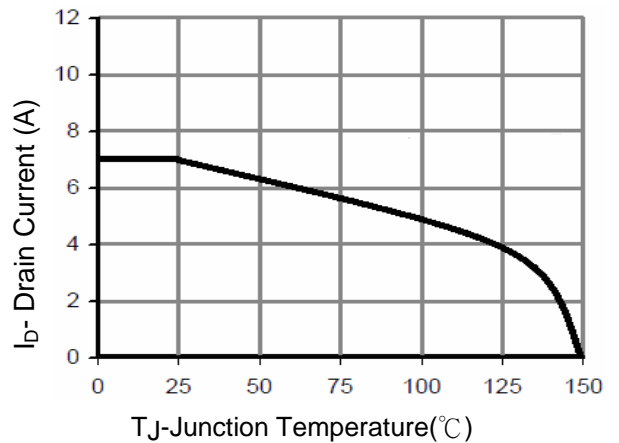


Fig. 10 ID Current- Junction Temperature

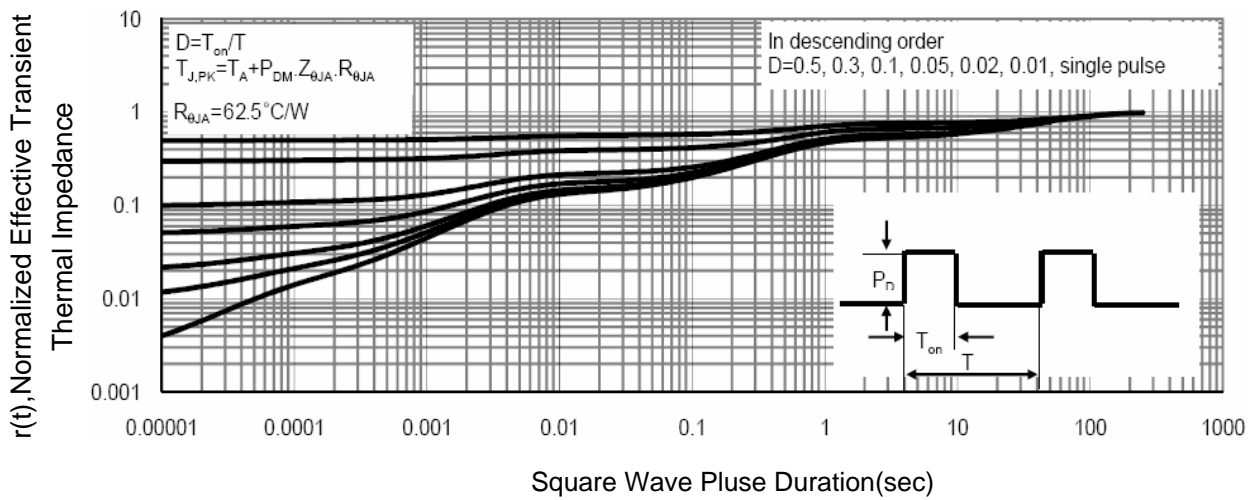


Fig.11 Normalized Maximum Transient Thermal Impedance

N&P-Channel complementary Power MOSFET

P-Channel Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-30V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-30V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-8A$	---	12	16	m Ω
		$V_{GS}=-4.5V, I_D=-5A$	---	18	25	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	-1.0	-1.5	-3	V
g_{fs}	Forward Transconductance	$V_{BS}=-5V, I_S=-5A$	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	$V_{DS}=-15V, V_{GS}=-10V, I_D=-8A$	---	26	---	nC
Q_{gs}	Gate-Source Charge		---	4.8	---	
Q_{gd}	Gate-Drain Charge		---	7.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=-15V, I_D=8A$ $V_{GS}=-10V, R_G=6\Omega$	---	10	---	ns
T_r	Rise Time		---	9	---	
$T_{d(off)}$	Turn-Off Delay Time		---	29	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, F=1\text{MHz}$	---	1880	---	pF
C_{oss}	Output Capacitance		---	210	---	
C_{rss}	Reverse Transfer Capacitance		---	186	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	-9	A
I_{SM}	Pulsed Source Current		---	---	-22	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. E_{AS} condition: $T_J=25^\circ\text{C}, V_{D0}=-15V, V_G=-10V, L=0.5\text{mH}, R_g=25\Omega, I_{AS}=-26A$

P-Channel Typical Electrical and Thermal Characteristics (Curves)

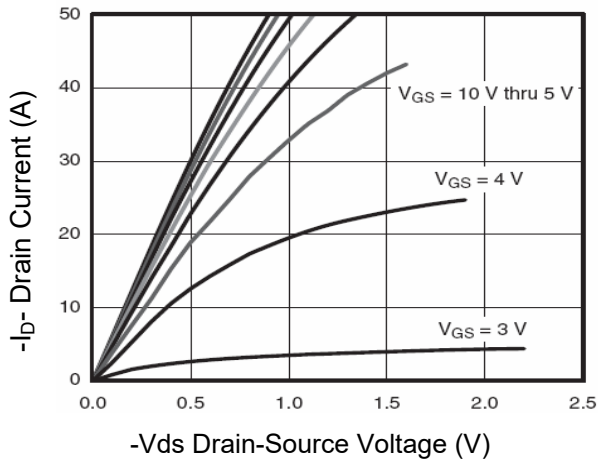


Fig.1 Output Characteristics

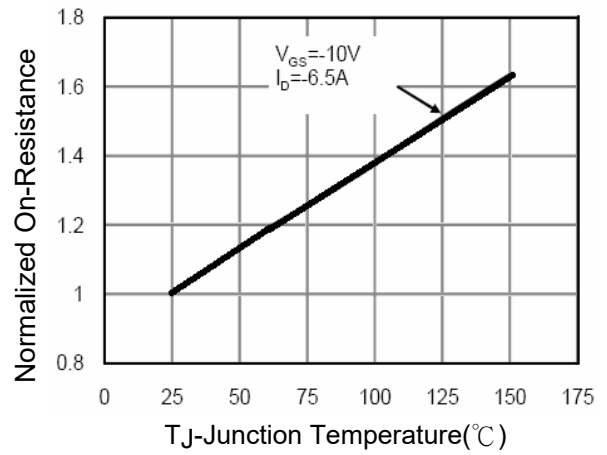


Fig. 4 Rds(on)-Junction Temperature

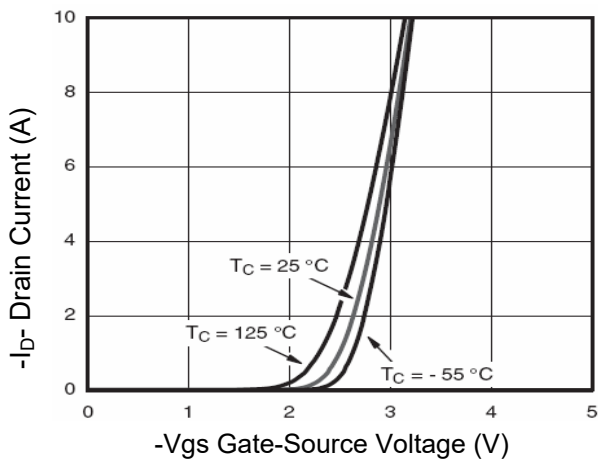


Fig. 2 Transfer Characteristics

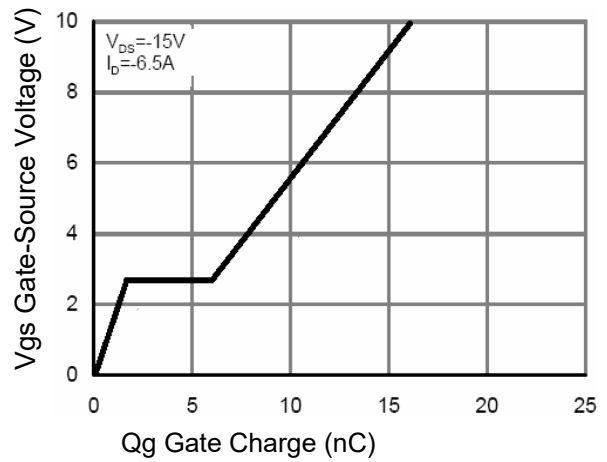


Fig. 5 Gate Charge

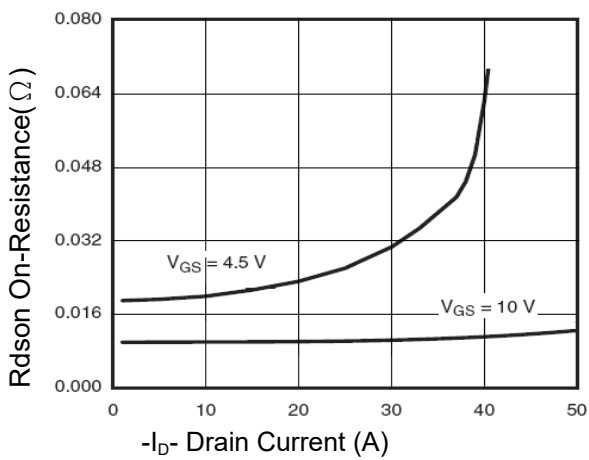


Fig. 3 Rds(on)- Drain Current

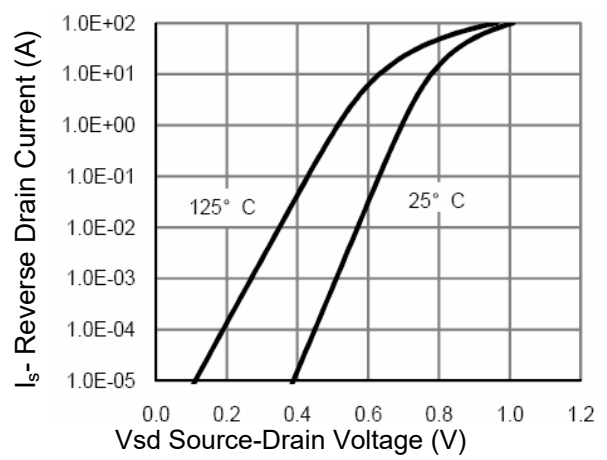


Fig. 6 Source- Drain Diode Forward

N&P-Channel complementary Power MOSFET

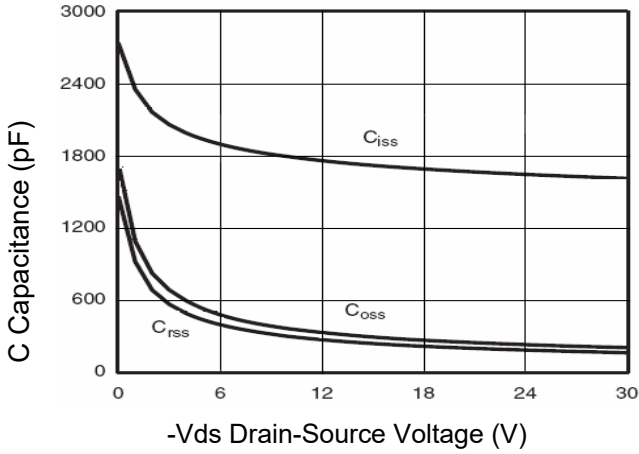


Fig.7 Capacitance vs Vds

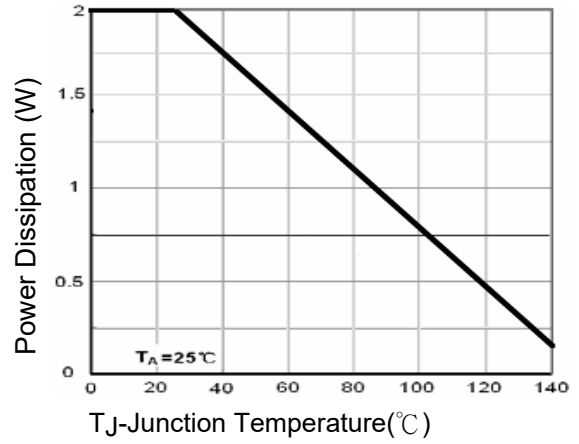


Fig. 8 Power De-rating

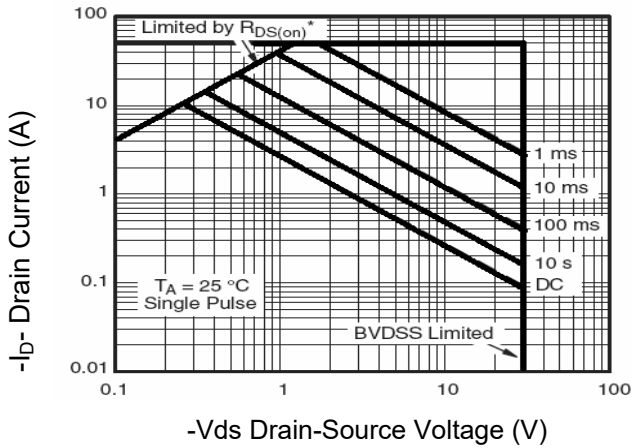


Fig.9 Safe Operation Area

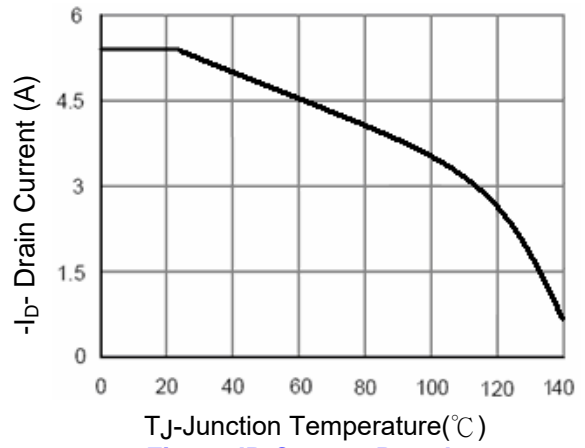


Fig. 10 ID Current Derating

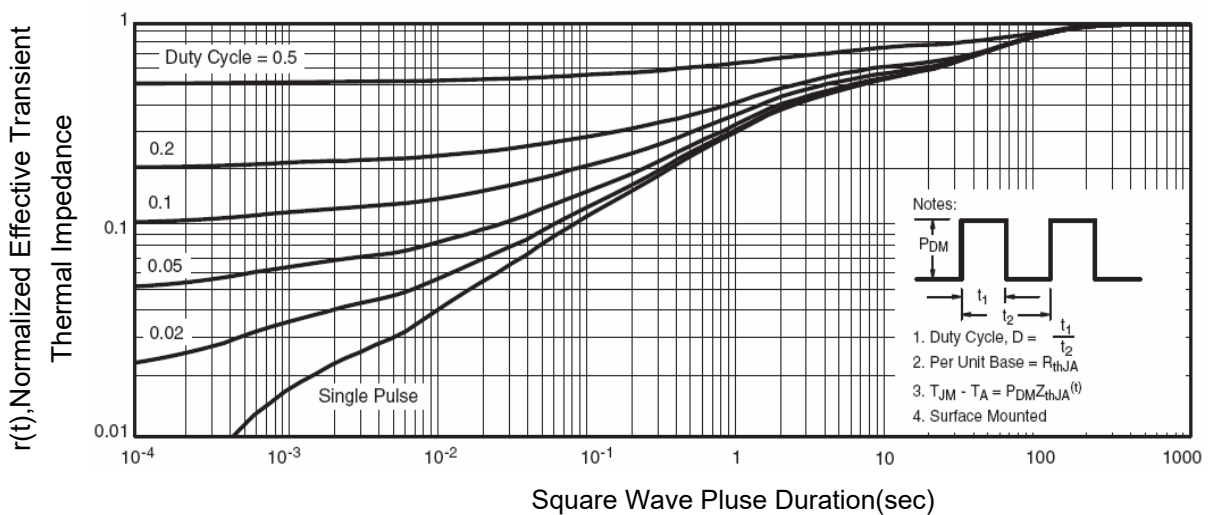


Fig.11 Normalized Maximum Transient Thermal Impedance

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